

**M.D UNIVERSITY**  
**SCHEME OF STUDIES AND EXAMINATION**  
**BE. II YEAR (ELECTRONICS & COMMUNICATION ENGINEERING)**  
**SEMESTER III**

**Modified 'E' Scheme effective from 2006-07**

Course No.	Course Title	Teaching Schedule				Marks of Class Work	Examination		Total Marks	Duration of Exam
		L	T	P	Total		Theory	Practical		
HUM-201-E	ECONOMICS (COMMON FOR ALL BR.)	3	1	-	4	50	100	-	150	3
MATH-201-E	MATHEMATICS - III (COMMON FOR ALL BR.)	3	2	-	5	50	100	-	150	3
EE-201-E	ELECTRICAL ENGINEERING MATERIALS & SEMI- CONDUCTOR DEVICES (EL,EI, IC,EE, EEE, AEI)	3	1	-	4	50	100	-	150	3
EE-203-E	NETWORK THEORY (EL,EI, IC,EE, EEE, AEI)	3	1	-	4	50	100	-	150	3
EE-205-E	ELECTROMECHANICAL ENERGY CONVERSION (EL,EI, IC, AEI)	3	1	-	4	50	100	-	150	3
CSE-201-E	DATA STRUCTURES & ALGORITHMS (CSE,EL,IT,EI, AEI)	3	1	-	4	50	100	-	150	3
EE-221-E	ELECTRICAL ENGINEERING MATERIALS & SEMI- CONDUCTOR DEVICES LAB (EL,EI, IC, AEI)	-	-	2	2	25	-	25	50	3
EE-223-E	NETWORK THEORY LAB (EL,EI, IC,EE, EEE, AEI)	-	-	2	2	25	-	25	50	3
EE-225-E	ELECTROMECHANICAL ENERGY CONVERSION LAB (EL,EI, IC, AEI)	-	-	3	3	50	-	50	100	3
EE-231-E	ELECTRICAL WORKSHOP (EL,EI, IC,EE,CHE, EEE, AEI)		-	2	2	25	-	25	50	3
	<b>TOTAL</b>	<b>18</b>	<b>7</b>	<b>9</b>	<b>34</b>	<b>425</b>	<b>600</b>	<b>125</b>	<b>1150</b>	

NOTE:

1. Students will be allowed to use non-programmable scientific calculator. However, sharing of calculator will not be permitted in the examination.

**HUM-201-E****ECONOMICS  
(COMMON FOR ALL BRANCHES)**L T P  
3 1 -Class Work : 50 Marks  
Theory : 100 Marks  
Total : 150 Marks  
Duration of Exam. : 3 Hrs.

COURSE OBJECTIVE : The purpose of this course is to :

1. Acquaint the student in the basic economic concepts and their operational significance and
2. Stimulate him to think systematically and objectively about contemporary economic problems.

**UNIT-I**

Definition of Economics - various definitions, Nature of Economic problem, Production possibility curve Economic laws and their nature. Relation between Science, Engineering, Technology and Economics.

**UNIT-II**

Concepts and measurement of utility, Law of Diminishing Marginal Utility, Law of equi-marginal utility - its practical application and importance.

**UNIT-III**

Meaning of Demand, Individual and Market demand schedule, Law of demand, shape of demand curve, Elasticity of demand, measurement of elasticity of demand, factors effecting elasticity of demand, practical importance & applications of the concept of elasticity of demand.

**UNIT-IV**

Meaning of production and factors of production; Law of variable proportions, Returns to scale, Internal and External economies and diseconomies of scale.

Various concepts of cost - Fixed cost, variable cost, average cost, marginal cost, money cost, real cost opportunity cost. Shape of average cost, marginal cost, total cost etc. in short run and long run.

**UNIT-V**

Meaning of Market, Types of Market - Perfect Competition, Monopoly, Oligopoly, Monopolistic Competition (Main features of these markets)

Supply and Law of Supply, Role of Demand & Supply in Price Determination and effect of changes in demand and supply on prices.

**UNIT-VI**

Nature and characteristics of Indian economy (brief and elementary introduction), Privatization - meaning, merits and demerits. Globalisation of Indian economy - merits and demerits. Elementary Concepts of VAT, WTO, GATT & TRIPS agreement.

**Books Recommended :****TEXT BOOKS :**

1. Principles of Economics : P.N. Chopra (Kalyani Publishers).
2. Modern Economic Theory – K.K. Dewett (S.Chand)

**REFERENCE BOOKS :**

1. A Text Book of Economic Theory Stonier and Hague (Longman's Landon)
2. Micro Economic Theory – M.L. Jhingan (S.Chand)
3. Micro Economic Theory - H.L. Ahuja (S.Chand)
4. Modern Micro Economics : S.K. Mishra (Pragati Publications)
5. Economic Theory - A.B.N. Kulkarni & A.B. Kalkundrikar (R.Chand & Co.)
6. Indian Economy : Rudar Dutt & K.P.M. Sundhram

NOTE: Eight questions are to be set atleast one question from each unit and the students will have to attempt five questions in all.

MATH-201-E  
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MATHEMATICS-III  
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(COMMON FOR ALL BRANCHES)

L	T	P	Class Work	:	50 Marks
3	2	-	Exam.	:	100 Marks
			Total	:	150 Marks
			Duration of exam.	:	3 Hours

**Part-A**

Fourier Series and Fourier Transforms : Euler's formulae, conditions for a Fourier expansion, change of interval, Fourier expansion of odd and even functions, Fourier expansion of square wave, rectangular wave, saw-toothed wave, half and full rectified wave, half range sine and cosine series.

Fourier integrals, Fourier transforms, Shifting theorem (both on time and frequency axes), Fourier transforms of derivatives, Fourier transforms of integrals, Convolution theorem, Fourier transform of Dirac-delta function.

**Part-B**

Functions of Complex Variable : Definition, Exponential function, Trigonometric and Hyperbolic functions, Logarithmic functions. Limit and Continuity of a function, Differentiability and Analyticity.

Cauchy-Riemann equations, necessary and sufficient conditions for a function to be analytic, polar form of the Cauchy-Riemann equations. Harmonic functions, application to flow problems. Integration of complex functions. Cauchy-Integral theorem and formula.

Power series, radius and circle of convergence, Taylor's Maclaurin's and Laurent's series. Zeros and singularities of complex functions, Residues. Evaluation of real integrals using residues (around unit and semi circle only).

**Part-C**

Probability Distributions and Hypothesis Testing : Conditional probability, Bayes theorem and its applications, expected value of a random variable. Properties and application of Binomial, Poisson and Normal distributions.

Testing of a hypothesis, tests of significance for large samples, Student's t-distribution (applications only), Chi-square test of goodness of fit.

Linear Programming : Linear programming problems formulation, Solving linear programming problems using (i) Graphical method (ii) Simplex method (iii) Dual simplex method.

TEXT BOOKS :

1. Advanced Engg. Mathematics : F Kreyszig.
2. Higher Engg. Mathematics : B.S. Grewal.

REFERENCE BOOKS :

1. Advance Engg. Mathematics : R.K. Jain, S.R.K.Iyenger.
2. Advanced Engg. Mathematics : Michael D. Greenberg.
3. Operation Research : H.A. Taha.
4. Probability and statistics for Engineers : Johnson. PHI.

Note: Examiner will set eight questions, taking two from Part-A, three from Part-B and three from Part-C. Students will be required to attempt five question taking atleast one from each part.

EE-201-E ELECTRICAL ENGINEERING MATERIALS AND SEMICONDUCTOR DEVICES

L T P	CLASS WORK	:	50
3 1 0	EXAM	:	100
	TOTAL	:	150
	DURATION OF EXAM	:	3 HRS

UNIT 1 CONDUCTING MATERIALS:

Review of energy bands, description of materials, drift velocity, collision time, Mean free path, mobility, conductivity, relaxation time, factors affecting conductivity of materials, types of thermal conductivity, Wiedmann-Franz law, super conductivity, effect of magnetic field, conducting materials, applications.

UNIT 2 DIELECTRIC MATERIALS:

Behaviour of dielectric materials in static electric field, Dipole moments, Polarization, Dielectric constant, Polarizability, Susceptibility, mechanisms of polarization, behaviour in alternating field, dielectric loss, loss tangent, types of dielectric & insulating materials, electrostriction, Piezo-electricity, Applications.

UNIT 3 MAGNETIC MATERIALS:

Permeability, Magnetic susceptibility, magnetic moment, Magnetization, Dipole moment, types of magnetic materials, Magnetostriction, eddy current & hysteresis losses, applications.

UNIT 4 SEMICONDUCTORS:

Review of Si and Ge as semiconducting materials, Continuity Equation, P-N junction, Drift & Diffusion, Diffusion & Transition capacitances of P-N junction.

UNIT 5 CONSTRUCTION AND CHARACTERISTICS OF DEVICES:

Brief introduction to Planar Technology for device fabrication., metal -semiconductor junctions (ohmic and non-ohmic), breakdown mechanisms in p-n junction, zener diode, electrical and optical excitation in diodes, LED, solar cells and photo-detectors.

UNIT 6 BIPOLAR AND MOS DEVICES :

BJT, UJT, JFET, MOSFETS

UNIT 7 POWER DEVICES :

Thyristor, Diac, Triac, GTO, IGBT, VMOS

TEXT BOOKS:

1. Electrical Engineering Materials: A.J. Dekker; PHI.
2. Solid State Electronic Devices : StreetMan & Banerjee; Pearson.
3. Electronic Devices & Circuits: Millman & Halkias; MGH.

REFERENCE BOOKS:

1. Electrical Engineering Materials: S.P Seth & P.V Gupta; Dhanpat Rai.
2. Text Book of Power Electronics : H.C.Rai; Galgoitia Publications.
3. Electronic Devices & Circuit Theory : Boylestad & Nashelsky; Pearson.
4. Semiconductor devices : Jaspreet Singh; John Wiley.

NOTE : Eight questions are to be set in all by the examiner taking at least one question from each unit. Students will be required to attempt five questions in all.

**EE-203-E****NETWORK THEORY**

L T P  
3 1 0

CLASS WORK	:	50
EXAM	:	100
TOTAL	:	150
DURATION OF EXAM	:	3 HRS

**UNIT 1 TRANSIENT RESPONSE :**

Transient Response of RC, RL, RLC Circuits to various excitation signals such as step, ramp, impulse and sinusoidal excitations using laplace transform.

**UNIT 2 NETWORK FUNCTIONS :**

Terminal pairs or Ports, Network functions for one-port and two-port networks, poles and zeros of Network functions, Restrictions on pole and zero Locations for driving point functions and transfer functions, Time domain behavior from the pole-zero plot.

**UNIT 3 CHARACTERISTICS AND PARAMETERS OF TWO PORT NETWORKS :**

Relationship of two-port variables, short-circuit Admittance parameters, open circuit impedance, parameters, Transmission parameters, hybrid parameters, relationships between parameter sets, Inter-connection of two port networks.

**UNIT 4 TOPOLOGY :**

Principles of network topology , graph matrices, network analysis using graph theory.

**UNIT 5 TYPES OF FILTERS AND THEIR CHARACTERISTICS :**

Filter fundamentals, high-pass, low-pass, band-pass, and band-reject Filters.

**UNIT 6 NETWORK SYNTHESIS :**

Positive real functions, synthesis of one port and two port networks, elementary ideas of Active networks.

**TEXT BOOKS:**

1. Network Analysis & Synthesis : Umesh Sinha; Satya Prakash Pub.
2. Network Analysis & Synthesis : F.F.Kuo; John Wiley & Sons Inc.

**REFERENCE BOOKS:**

1. Introduction to modern Network Synthesis : Van Valkenburg; John Wiley
2. Network Analysis: Van Valkenburg; PHI
3. Basic circuit theory:Dasoer Kuh; McGraw Hill.
4. A Course in Electrical Circuit Analysis by Soni & Gupta; Dhanpat Rai Publication.
5. Circuit Analysis : G.K. Mithal; Khanna Publication.
6. Networks and Systems : D.Roy Choudhury; New Age International.

NOTE: Eight questions are to be set in all by the examiner taking at least one question from each unit. Students will be required to attempt five questions in all.

**EE-205-E****ELECTROMECHANICAL ENERGY CONVERSION**

L T P	CLASS WORK	:	50
3 1 0	EXAM	:	100
	TOTAL	:	150
	DURATION OF EXAM	:	3 HRS

**UNIT 1 MAGNETIC CIRCUITS AND INDUCTION:**

Magnetic Circuits, Magnetic Materials and their properties, static and dynamic emfs and force on current carrying conductor, AC operation of Magnetic Circuits, Hysteresis and Eddy current losses.

**UNIT 2 PRINCIPLES OF ELECTROMECHANICAL ENERGY CONVERSION:**

Force and torque in magnetic field system, energy balance, energy and force in singly excited magnetic field system, concept of co-energy, forces and torques in system with permanent magnets, dynamic equation.

**UNIT 3 TRANSFORMERS :**

Basic theory, construction, operation at no-load and full-load, equivalent circuit, phasor diagram, O.C. and S.C. tests for parameters determination, efficiency and regulation, auto-transformer, introduction to three-phase transformer ; Current and Potential Transformers : Principle, construction, analysis and applications.

**UNIT 4 DC MACHINES :**

Basic theory of DC generator, brief idea of construction, emf equation, load characteristics, basic theory of DC motor, concept of back emf, torque and power equations, load characteristics, starting and speed control of DC motors, applications.

**UNIT 5 INDUCTION MOTOR:**

Basic theory, construction, Phasor diagram, Equivalent circuit, Torque equation, Load characteristics, starting and speed control of induction motor, Introduction to single phase Induction motor and its applications, Fractional H.P. Motors, Introduction to stepper, servo reluctance and universal motors.

**UNIT 6 SYNCHRONOUS MACHINES:**

Construction and basic theory of synchronous generator, emf equation, model of generator, Phasor diagram, Regulation, Basic theory of synchronous motor, v-curves, synchronous condenser, applications.

**TEXT BOOK:**

1. Electrical Machines: Nagarath and Kothari; TMH

**REFERENCE BOOKS:**

1. Electrical Machines :P.S. Bimbhra; Khanna
2. Electrical Machines: Mukherjee and Chakravorti; Dhanpat Rai & Sons
3. Electrical Technology (Vol-II) : B.L Theraja; S. Chand.

**NOTE:** Eight questions are to be set in all by the examiner taking at least one question from each unit. Students will be required to attempt five questions in all.

**CSE-201 E****Data Structures & Algorithms**

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3     1     -

Class Work:     50  
Exam:            100  
Total:            150  
Duration of Exam: 3 Hrs.

**Unit-1: Introduction to Data Structures:** Definition of data structures and abstract data types, Static and Dynamic implementations, Examples and real life applications; The Stacks : Definition, Array based implementation of stacks, Linked List based implementation of stacks, Examples : Infix, postfix, prefix representation, Conversions, Applications.

**Unit-2: Queues and Lists:** Definition, Array based implementation of Queues / Lists, Linked List implementation of Queues / Lists, Circular implementation of Queues and Singly linked Lists, Straight / circular implementation of doubly linked Queues / Lists, Priority Queues, Applications.

**Unit-3: Trees:** Definition of trees and Binary trees, Properties of Binary trees and Implementation, Binary Traversal pre-order, post order, In- order traversal, Binary Search Trees, Implementations, Threaded trees, Balanced multi way search trees, AVL Trees, Implementations

**Unit-4: Graphs:** Definition of Undirected and Directed Graphs and Networks, The Array based implementation of graphs, Adjacency matrix, path matrix implementation, The Linked List representation of graphs, Shortest path Algorithm, Graph Traversal – Breadth first Traversal, Depth first Traversal, Tables : Definition, Hash function, Implementations and Applications.

**Unit-5: Running time:** Time Complexity, Big – Oh - notation, Running Times, Best Case, Worst Case, Average Case, Factors depends on running time, Introduction to Recursion, Divide and Conquer Algorithm, Evaluating time Complexity.

**Unit-6: Sorting Algorithms :** Introduction, Sorting by exchange, selection, insertions : Bubble sort, Straight selection sort, Efficiency of above algorithms,; Shell sort, Performance of shell sort, Merge sort, Merging of sorted arrays & Algorithms; Quick sort Algorithm analysis,

**Heap sort:** Heap Construction, Heap sort, bottom – up, Top – down Heap sort approach;

**Searching Algorithms:** Straight Sequential Search, Binary Search (recursive & non-recursive Algorithms)

**Text Book:**

Data Structures using C by A. M. Tenenbaum, Langsam, Moshe J. Augentem, PHI Pub.

**Reference Books:**

- Data Structures and Algorithms by A.V. Aho, J.E. Hopcroft and T.D. Ullman, Original edition, Addison-Wesley, 1999, Low Priced Edition.
- Fundamentals of Data structures by Ellis Horowitz & Sartaj Sahni, Pub, 1983, AW
- Fundamentals of computer algorithms by Horowitz Sahni and Rajasekaran.
- Data Structures and Program Design in C By Robert Kruse, PHI,
- Theory & Problems of Data Structures by Jr. Seymour Lipschetz, Schaum's outline by TMH
- Introduction to Computers Science -An algorithms approach , Jean Paul Tremblay, Richard B. Bunt, 2002, T.M.H.
- Data Structure and the Standard Template library – Willam J. Collins, 2003, T.M.H

**Note:** Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

**EE-221-E ELECTRICAL ENGINEERING MATERIALS AND SEMICONDUCTOR DEVICES LAB**

L T P	CLASS WORK	:	25
0 0 2	EXAM	:	25
	TOTAL	:	50
	DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS :

1. To study V-I characteristics of diode, and its use as a capacitance.
2. Study of the characteristics of transistor in Common Base configuration.
3. Study of the characteristics of transistor in Common Emitter configuration.
4. Study of V-I characteristics of a photo-voltaic cell.
5. Study of characteristics of MOSFET/JFET in CS configuration.
6. To plot characteristics of thyristor.
7. To plot characteristics of UJT .
8. To plot characteristics of diac & Triac.
9. Study of loss factor in a dielectric by an impedance bridge.
10. Study of photo-resist in metal pattern for planar technology/PCB technology.

NOTE : Ten experiments are to be performed, out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus.

**EE-223-E**

**NETWORK THEORY LAB**

L T P  
0 0 2

CLASS WORK	:	25
EXAM	:	25
TOTAL	:	50
DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS :

1. Transient response of RC circuit.
2. Transient response of RL circuit.
3. To find the resonance frequency, Band width of RLC series circuit.
4. To calculate and verify "Z" parameters of a two port network.
5. To calculate and verify "Y" parameters of a two port network.
6. To determine equivalent parameter of parallel connections of two port network.
7. To plot the frequency response of low pass filter and determine half-power frequency.
8. To plot the frequency response of high pass filter and determine the half-power frequency.
9. To plot the frequency response of band-pass filter and determine the band-width.
10. To calculate and verify "ABCD" parameters of a two port network.
11. To synthesize a network of a given network function and verify its response.
12. Introduction of P-Spice

NOTE : Ten experiments are to be performed, out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus.

**EE-225-E**

**ELECTROMECHANICAL ENERGY CONVERSION LAB**

L T P  
0 0 3

CLASS WORK	:	50
EXAM	:	50
TOTAL	:	100
DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS:

1. To find turns ratio and polarity of a single phase transformer.
2. To perform open and short circuit tests on a single phase transformer.
3. To perform Sumpner's back to back test on single phase transformers.
4. Parallel operation of two single phase transformers.
5. Study of construction of a DC machine.
6. To plot O.C.C of a DC shunt generator and find its Critical Resistance.
6. To perform direct load test of a DC motor.
8. Speed control of a DC motor by armature control and field control methods.
9. To perform open circuit and block rotor tests of an induction motor.
10. Star-delta starting of a three phase induction motor.
11. Plot O.C.C of a synchronous generator.
12. To plot V-curve of a synchronous motor.

NOTE: Ten experiments are to be performed, out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus.

**EE-231-E****ELECTRICAL WORKSHOP**

L T P	CLASS WORK	:	25
0 0 2	EXAM	:	25
	TOTAL	:	50
	DURATION OF EXAM	:	3 HRS

**LIST OF EXPERIMENTS:**

1. Introduction of tools, electrical materials, symbols and abbreviations.
2. To study stair case wiring.
3. To study house wiring i.e., batten, cleat, casing-caping and conduit wirings.
4. To study fluorescent tube light.
5. To study high pressure mercury vapour lamp (H.P.M.V).
6. To study Sodium lamp.
7. To study repairing of home appliances such as heater, electric iron, fans etc.
8. To study construction of moving iron, moving coil, electrodynamic & induction type meters.
9. To design & fabricate single phase transformer.
10. To study fuses, relays, contactors, MCBs and circuit breakers.
11. Insulation testing of electrical equipments.
12. To design, fabricate a PCB for a circuit, wire-up and test.

NOTE: Ten experiments are to be performed, out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution.